

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SHUBHENDU S. MUKHERJEE

Application No.:

Filed:

For: **A METHOD AND SYSTEM FOR
COALESCING COHERENCE
MESSAGES**

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

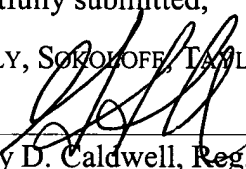
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: March 08, 2004



Gregory D. Caldwell, Reg. No. 39,926

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (503) 684-6200

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Derek S. Watson

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Date

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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Application Number | |
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| | | | | Art Unit | |
| | | | | Examiner Name | |
| Sheet | 1 | of | 1 | Attorney Docket Number | |
| 42P18930 | | | | | |

| NON PATENT LITERATURE DOCUMENTS | | | | |
|---------------------------------|-----------------------|---|----------------|--|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² | |
| | 1 | S. Mukherjee et al., "Using Prediction to Accelerate Coherence Protocols", Computer Sciences Dept., University of Wisconsin | | |
| | 2 | M. Martin et al., "Token Coherence: Decoupling Performance and Correctness", Proc. of the 30th Annual International Symp. on Computer Architecture, 2003 IEEE | | |
| | 3 | S. Mukherjee et al., Compaq Computer Corp., "The Alpha 21364 Network Architecture", 2002 IEEE | | |
| | 4 | A. Charlesworth, Sun Microsystems, "The Sun Fireplace Interconnect", 2002 IEEE | | |
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003.
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